

Jetter Nano Series PLC

HMI Factory Setting:

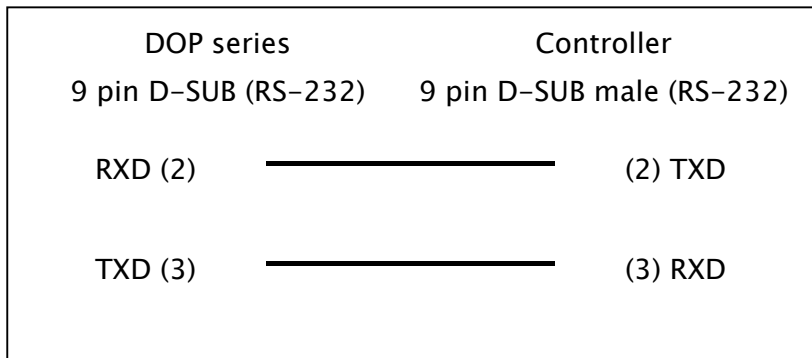
Baud rate: 9600. 8. Even. 1(RS-232)

Controller Station Number: 0 (no PLC station number in protocol, one HMI to one PLC connection)

Control Area / Status Area: WR0/WR10

Connection

a. RS-232 (DOP-A/AE/AS, DOP-B Series)



Definition of PLC Read/Write Address

a. Registers

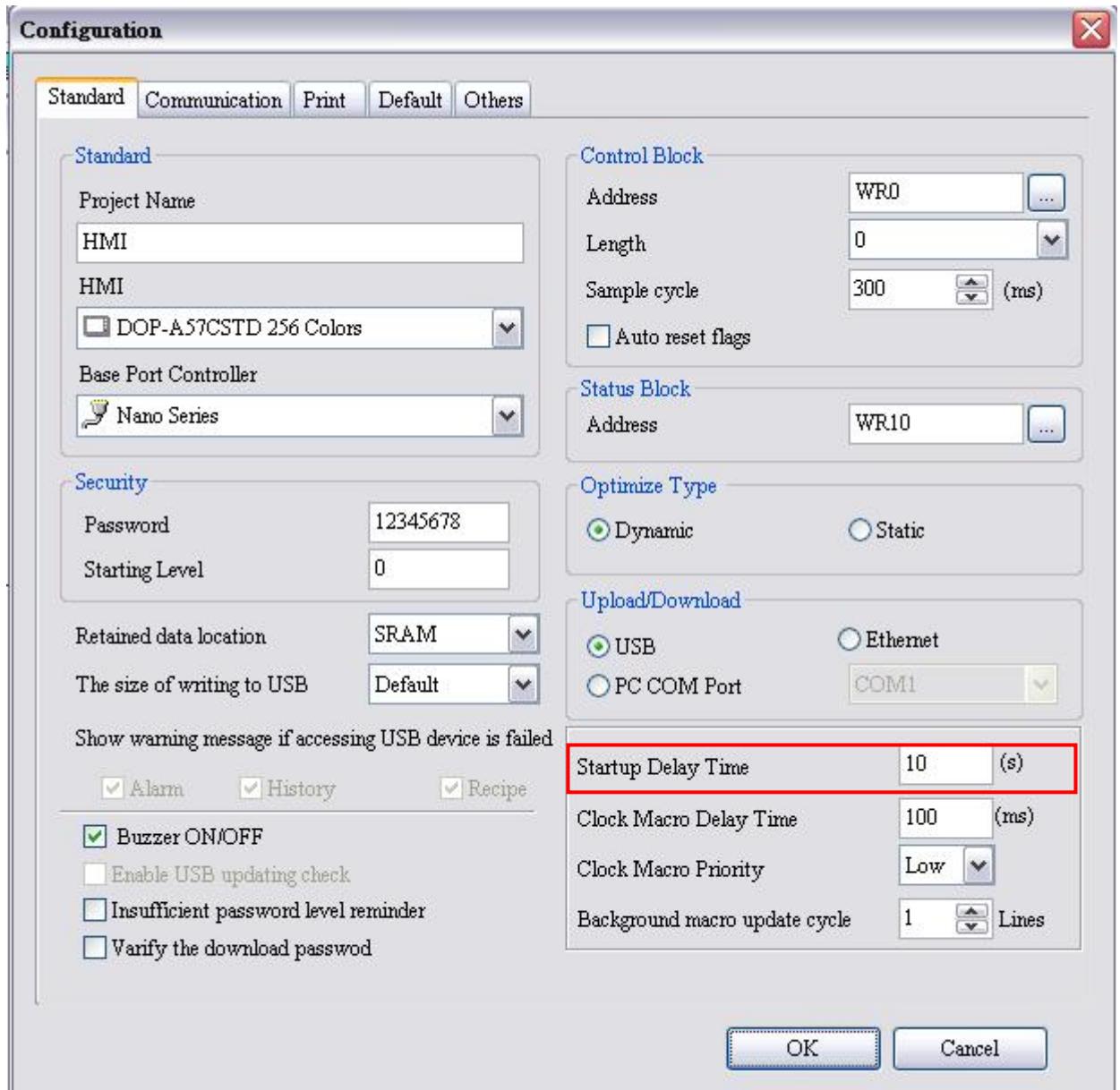
Type	Format	Read/Write Range	Data Length	Note
	Word No. (n)			
16 Bits Register	WRn	WR0 - WR32767	16 Bits	5
32 Bits Register	Rn	R0 - R32767	24 Bits	3 , 6 , 7

b. Contacts

Type	Format	Read/Write Range	Note
	Word No. (n) Bit No. (b)		
Input Relay	Inbb	I101 - I3208	
Output Relay	Onbb	O101 - O3208	
Flag Relay	Fb	F0 - F32767	

NOTE

- 1) In general, every register occupies a maximum 24 Bits. However, some registers occupies only 8 Bits.
- 2) Jetter Nano Series PLC requires longer time at initial start, therefore it is recommended to set startup delay time greater than 10 (s).



- 3) When the register R is used for Double Word device, please set the format as signed format. (The default format in Screen Editor is signed format.)
- 4) Please be aware the pin definition for RS232 in this PLC series is different than the standard RS232, do not mistake.
- 5) **WR** only occupies Bit0~Bit15 of every register.

- 6) R occupies 24 Bits of every register and Bit24~Bit31 set to 0 by default setting.
- 7) Decimal notation range from -8388608 to +8388607 ; hexadecimal notation range from 0x000000 to 0xFFFFFFFF.
- 8) The difference between WRn and Rn register:
 1. When using devices that the data length is in Word, only Bit 0 ~ 15 are valid for both of WRn and Rn registers.
 2. When using devices that the data length is in Double Word, if the read/write address format is set to WRn, the Bit 0 ~ 15 of WRn register is the low word of a read/write value, the Bit 0 ~ 15 of WRn+1 register is the high word of a read/write value. If the read/write address format is set to Rn, only Bit 0 ~ 23 are valid for Rn registers.
(Notice: As the Jetter controller is a 24-bit format controller, the valid setting range is 24 Bits (16777215). If setting exceeds this range, HMI will stop read/write operation and show “.....Value is Incorrect” on the screen.
 3. When using devices that the data length is in m Words, if the read/write address format is set to WRn, the Bit 0 ~ 15 of WRn register is the lowest word of a read/write value and the Bit 0 ~ 15 of WRn+m-1 register is the highest word of a read/write value. If the read/write address format is set to Rn, the Bit 0 ~ 23 of Rn register is the lowest word of a read/write value and the Bit 0 ~ 23 of Rn+1 register is the highest word of a read/write value. Each register is regards as a “Double Word”. The value of Bit24 ~ Bit31 is 0.