



Precision Machinery Research & Development Center

## CE Verification of Certificate

Issued to :

**Delta Electronics, Inc.**

7F. -6, No.210, Gongyequ 38th Rd., Xitun Dist., Taichung City 40768, Taiwan R.O.C.

In respect of manufactured by :

**Delta Electronics, Inc.**

Product : MOTION CONTROL HUB 1-GEN ECAT PCI\*2 WB

Model Name : MH1-C50E

Series Model : MH1-C50E Series (Annex A)

The verification of compliance refers to the above mentioned product that is chosen to address the electromagnetic compatibility of EN 55032:2015 and EN 55024:2010, which also fulfils the requirements of the electromagnetic compatibility Directive 2014/30/EU, this is to certify that the specimen is in conformity with the assessment requirement mentioned above, this verification does not imply assessment of production of the product.

Verification No.: 106R0017-146

Test Report No.: N3E11-106R0017-146

Issued Date: December 09, 2016

Expires: December 09, 2019

Approved by PMC



Address : No.27, 37<sup>th</sup> Road, Taichung Industrial Park, Taichung, Taiwan, R.O.C.

Tel : 886-4-23599009 Fax : 886-4-23598847



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 Annex A Certificate 106R0017-146

**Rule**

<b>Example</b>	MH1	-	C	5	0	E	-	A	0	0	D	0
<b>Code</b>	1		2	3	4	5		6	7	8	9	10

**Definition**

Code	Definition	Char	Description	
1	Product type	3	MH1 = Motion Control Hub 1 <sup>st</sup> Generation	
2	CPU type	1	A = Intel Atom Series C = Intel Core series L = LSI/ASIC M = Intel Atom Series Slim type R = ARM S = other x86 system	
3	CPU manufacturer	1	[Code 2 = A/M Intel Atom Series] 1: Atom Bay Trail [Code 2 = C Intel Core Series] 3: Core i3 5: Core i5 7: Core i7 [Code 2 = P PowerPC CPU manufacturer] 1: Freescale [Code 2 = R ARM CPU manufacturer] 1: TI 2: Freescale 3: Altera 4: NXP [Code 2 = S other x86 CPU manufacturer] 1: DMP 2: AMD 3: VIA [Code 2 = L LSI/ASIC manufacturer] 1: NPM	
4	CPU capability	1	[Code 2,3 = A/M,1 Intel Atom Series] 0: E3825 1: E3827 2: E3845 [Code 2,3 = C,5 Intel Core Series] 0: i5-3610 [Code 2,3 = any] x = Type x (x=0~9)	
5	Extension interface	1	[Extension interface]	
			C = Camera Link	D = DMCNET
			E = EtherCAT	F = IEEE 1394 (FireWire)
			G = Giga-LAN	M = Motion.NET
			N = N/A	P = PC/104





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6	Slot interface	1	<b>[Slot interface]</b> A = 2 PCI slot B = 2 PCIe slot x1+x1 C = 2 PCIe slot x4+x1 N = no extension slot																																																								
7	DRAM & micro-SD card (eMMC) options	1	<b>[DRAM/micro-SD(eMMC) options]</b> <table border="1"> <tr> <td></td> <td>DRAM</td> <td>None</td> <td>2GB</td> <td>4GB</td> <td>8GB</td> <td>4GB +4GB</td> <td>8GB +8GB</td> </tr> <tr> <td>SD</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>eMMC</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>None</td> <td></td> <td></td> <td>1</td> <td>0</td> <td></td> <td>3</td> <td></td> </tr> <tr> <td>4GB</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>8GB</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>4GB+4GB</td> <td></td> <td>2</td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>		DRAM	None	2GB	4GB	8GB	4GB +4GB	8GB +8GB	SD								eMMC								None			1	0		3		4GB								8GB								4GB+4GB		2					
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10	Customer version	1	x = Type x (x=0~9)																																																								

